

2133

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No. **#**
15483

In Re Application Of: **Hideharu Ozaki**

OC
11/11/02

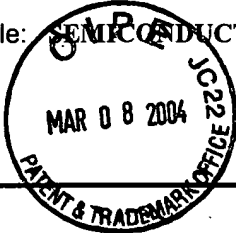
Serial No.
10/063,472

Filing Date
April 26, 2002

Examiner
James C. Kerveros

Group Art Unit
2133

Title: **SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND DEVICE FOR TESTING SAME**



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37 CFR 1.97(b)

1. ☐ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

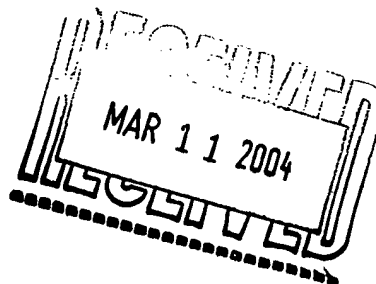
37 CFR 1.97(c)

2. ☒ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☒ the statement specified in 37 CFR 1.97(e);

OR

☐ the fee set forth in 37 CFR 1.17(p).



TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT
(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
15483

In Re Application: **Hideharu Ozaki**

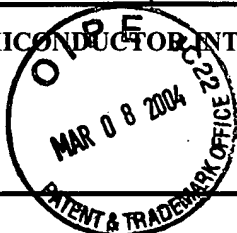
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SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND DEVICE FOR TESTING SAME



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Payment of Fee

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(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☒ The Director is hereby authorized to charge and credit Deposit Account No. **19-1013/SSMP** as described below.
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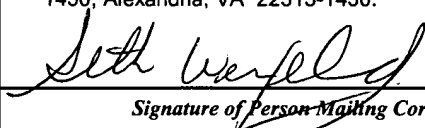
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Seth Weinfeld

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Signature

Dated: **March 3, 2004**

Seth Weinfeld
Registration No. 50,929
Scully, Scott, Murphy & Presser
400 Garden City Plaza
Garden City, NY 11530
(516) 742-4343

SW:lf

CC:



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Hideharu Ozaki

Examiner: James C. Kerveros

Serial No.: 10/063,472

Art Unit: 2133

Filed: April 26, 2002

Docket: 15483

For: SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE AND DEVICE
FOR TESTING SAME

Dated: March 3, 2004

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Confirmation No.: 1170

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P. O. Box 1450
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INFORMATION DISCLOSURE STATEMENT

Sir:

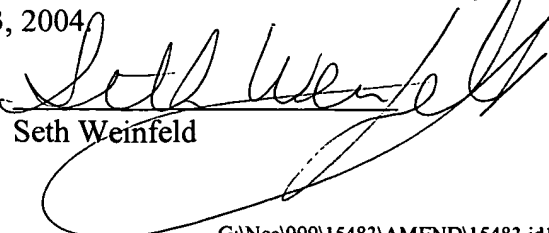
In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following references, which are also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Japanese Patent Publication No. 8-201481, published September 8, 1996, with Patent Abstract Publication of Japan;
2. European Publication No. EP 1 024 367 A, published August 2, 2000;
3. European Publication No. EP 0 806 837 A2, published November 12, 1997; and

CERTIFICATE OF MAILING UNDER 37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on March 3, 2004.

Dated: March 3, 2004

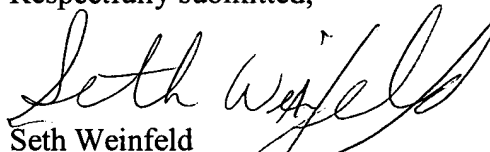

Seth Weinfeld

4. United States Patent No. 5,519,715, issued May 21, 1996 to Hao, et al.

The references were cited in a European Search Report dated December 23, 2003 received from the European Patent Office. Applicant is submitting copies of the above-cited references, together with a copy of the Search Report. The relevance of the above-identified references has been described in the Search Report.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R§1.97(c)(1) and (e)(1), a statement is enclosed.

Respectfully submitted,



Seth Weinfeld
Registration No. 50,929

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400 Garden City Plaza
Garden City, New York 11530
(516) 742-4343

SW:lf

**STATEMENT UNDER 37 CFR 1.97(e) ACCOMPANYING
INFORMATION DISCLOSURE STATEMENT**

Docket No.
15483

In Re Application Of: **Hideharu Ozaki**

Serial No.
10/063,472

Filing Date
April 26, 2002

Examiner
James C. Kerveros

Group Art Unit
2133

Invention:

SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND DEVICE FOR TESTING SAME



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TO THE COMMISSIONER FOR PATENTS:

This is a statement under the provisions of 37 CFR 1.97(e) in the above-identified application.

Check applicable statement herebelow:

Statement Under 37 CFR 1.97(e)(1)

- ☒ Each item of information contained in the accompanying Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of the Information Disclosure Statement.

Statement Under 37 CFR 1.97(e)(2)

- ☐ No item of information contained in the accompanying Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the undersigned person, after making reasonable inquiry, no item of information contained in the accompanying Information Disclosure Statement was known to any individual designated in 37 CFR 1.56(c) more than three months prior to the filing of the Information Disclosure Statement.

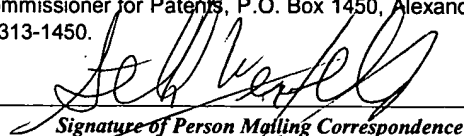

Signature

Dated: **March 3, 2004**

Seth Weinfeld
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Garden City, NY 11530
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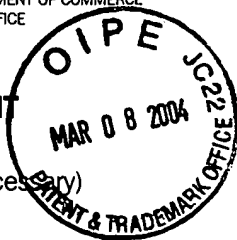
Seth Weinfeld

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**LIST OF PRIOR ART
CITED BY APPLICANT**

(Use several sheets if necessary)

**Atty. Docket No.**
15483**Serial No.**
10/063,472**Applicant**
Hideharu Ozaki**Filing Date**
April 26, 2002**Group**
2133**U.S. PATENT DOCUMENTS**

EXAMINER INITIAL*		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE (if appropriate)
	AA	5,519,715	5/21/96	Hao, et al.			
	AB						
	AC						
	AD						
	AE						

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		Foreign Document Number	Date	Country	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
		8-201481	9/8/96	Japan				
		EP 1 024 367 A,	8/2/00	EPO				
		EP 0 806 837 A2	11/12/97	EPO				

OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

EXAMINER**DATE CONSIDERED**

* **EXAMINER:** Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.